## Power sequencing test solution for FPGAs, MCUs and CPUs Parallel measurement of up to 20 channels

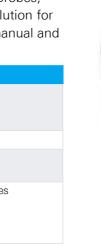
## Challenge

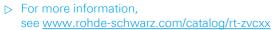
Processing components such as FPGAs, MCUs and CPUs have specific power supply requirements. Their multiple supplies need to power-up and power-down in a specified time sequence in order to avoid damage. The supply voltage tolerances are tight and require a carefully designed power supply. Accurate testing and characterization of the power sequencing and the supply voltage tolerances is necessary to enable an impeccable circuit design and flawless operation of the application.

### **Solution**

The R&S°RT-ZVC multi-channel probe adds 4 current and 4 voltage channels with 18-bit resolution to an Rohde & Schwarz oscilloscope. A single R&S°RTE or R&S°RTO oscilloscope supports two R&S°ZVC probes, for a total of 20 synchronous channels for power sequence measurements. Thanks to the high accuracy of the R&S°ZVC probes, they can also be used to verify voltage tolerances. This makes it an excellent solution for power-up and power-down testing and for monitoring power stability in both manual and automated test scenarios.

Your benefit	Features		
Multiple channels and superior performance	<ul> <li>1 2/4 current and 2/4 voltage channels (R&amp;S°RT-ZVC02/04)</li> <li>1 18-bit ADC for each input channel</li> <li>1 MHz bandwidth, 5 Msample/s sampling rate for each channel</li> </ul>		
Excellent accuracy	■ 0.1% voltage and 0.2% current DC measurement accuracy		
Full-scale measurement ranges	<ul> <li>4 voltage measurement ranges from 1.88 V to 15 V</li> <li>6 current measurement ranges from 4.5 μA to 10 A</li> </ul>		
Ideal for power sequence testing	<ul> <li>Straightforward visualization of ramp-up and ramp-down sequences</li> <li>Simple delay measurement between individual channels</li> <li>Validation of inrush current limit</li> <li>Power rail tolerance verification</li> </ul>		







## Up to 20 channels | Internal Control of Con

Up to 20 voltages can be analyzed in parallel using two R&S\*RT-ZVC probes attached to a 4-channel oscilloscope (current channels operating as high-sensitivity voltmeter in external shunt mode).

# O.1% accuracy for voltage tolerance verification Set voltage: 3.3 V Mean 3.3045 V Gated measurement Set voltage: 1.2 V Mean 1.2061 V The 0.1% accuracy of the voltage measurement channels allows verification of the tight supply voltage tolerance

Popular options/accessories

Digital oscilloscope, 500 MHz,

5 Gsample/s, 4 channels

20 Gsample/s, 4 channels

Multi-channel power probe

Digital oscilloscope, 1 GHz, 10 Gsample/s,

High performance oscilloscope, 4 GHz,

2 x 2 / 2 x 4 18-bit voltage/current probe,

for R&S®RTO2000 and R&S®RTE1000

Extended cable set for R&S®RT-ZVC,

Extended cable set for R&S®RT-ZVC.

Extended cable set for R&S°RT-ZVC, 4 mm probing, 1 current and voltage lead,

Extended cable set for R&S®RT-ZVC.

Extended cable set for R&S®RT-ZVC,

5 micro clips for reliable contacting

BNC connector, 1 current and voltage

4 mm probing, 1 current and voltage lead,

PCB probing, 1 current and voltage lead,

PCB probing, 1 current and voltage lead.

Digital extension port supporting R&S\*RT-ZVC02 and R&S\*RT-ZVC04

R&S®RTO2014

R&S®RTE1054

R&S®RTP044

R&S®RT-ZVC02/

R&S®RT-ZVC04

R&S®RTO-B1E

R&S®RTE-B1E

R&S®RTP-B1E

R&S®RT-ZA30

R&S®RT-ZA35

R&S®RT-7A31

R&S®RT-ZA34

R&S®RT-ZA37

R&S®RT-ZA4

Base unit

4 channels

hardware

Voltage probes

length: 32 cm

length: 1 m

length: 32 cm

length: 1 m

lead, length: 16 cm

## Dedicated accessories for high-quality measurements

windows in FPGA and CPU power supplies.



Wide range of accessories provides flexibility in contacting on PCB boards or other electronic components. For higher voltage ranges, BNC connector cables are available, which allow the use of standard passive or active differential probes.

900 mV 800 mV 700 mV	-		
- 600 mV	1	•	
500 mV	Rise time	2 _ X	
400 mV	Delay	86.498 ms	
300 mV	<b>#</b>		
200 mV	#		
100 mV	/ <del>‡</del>		
1200-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1			٠.,
-100 mY			40 ms/dir
Diagram1: M1 🗶	*		
	Ī.,		
1)·V	1		Т
-800 mV	‡		_
-2 V	Ī.,		40 ms/div

The integrated analysis and math functions allow detailed monitoring of voltage slew rates, delays and min./max. voltages.

Rohde & Schwarz GmbH & Co. KG | Europe, Africa, Middle East +49 89 4129 12345 | North America 1 888 TEST RSA (1 888 837 87 72)

Latin America +1 410 910 79 88 | Asia Pacific +65 65 13 04 88 | China +86 800 810 82 28 / +86 400 650 58 96

www.rohde-schwarz.com | customersupport@rohde-schwarz.com

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